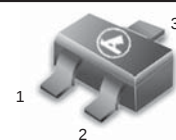


Dual Switching Diodes

BAW56WT1



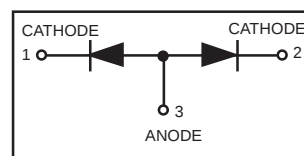
CASE 419-04, STYLE 4
SOT-323 (SC-70)

DEVICE MARKING

BAW56WT1 = A1

MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

Rating	Symbol	Max	Unit
Reverse Voltage	V_R	70	Vdc
Forward Current	I_F	200	mAdc
Peak Forward Surge Current	$I_{FM(surge)}$	500	mAdc



THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board ⁽¹⁾ $T_A = 25^\circ\text{C}$	P_D	200	mW
Derate above 25°C		1.6	mW/ $^\circ\text{C}$
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	0.625	$^\circ\text{C}/\text{W}$
Total Device Dissipation Alumina Substrate ⁽²⁾ $T_A = 25^\circ\text{C}$	P_D	300	mW
Derate above 25°C		2.4	mW/ $^\circ\text{C}$
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	417	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature	T_J, T_{stg}	-55 to +150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
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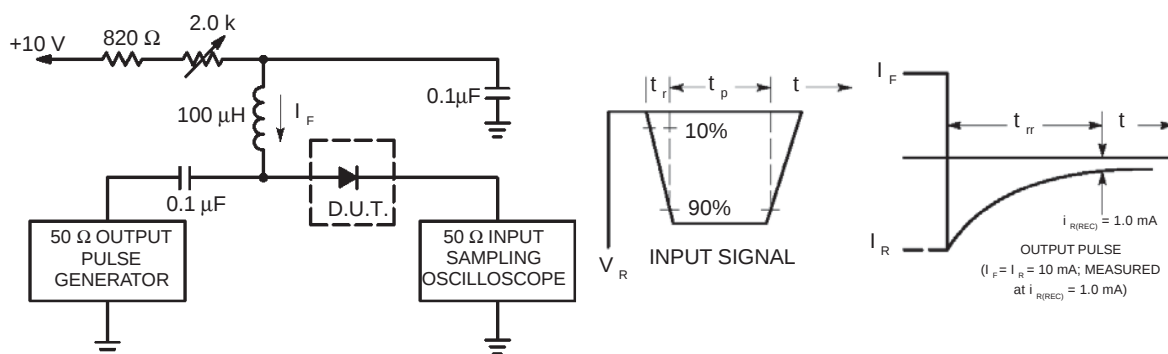
OFF CHARACTERISTICS

Reverse Breakdown Voltage ($I_{BR} = 100 \mu\text{Adc}$)	$V_{(BR)}$	70	—	Vdc
Reverse Voltage Leakage Current ($V_R = 25 \text{ Vdc}, T_J = 150^\circ\text{C}$)	I_R	—	30	μAdc
($V_R = 70 \text{ Vdc}$)		—	2.5	
($V_R = 70 \text{ Vdc}, T_J = 150^\circ\text{C}$)		—	50	
Diode Capacitance ($V_R = 0, f = 1.0 \text{ MHz}$)	C_D	—	2.0	pF
Forward Voltage ($I_F = 1.0 \text{ mAdc}$)	V_F	—	715	mVdc
($I_F = 10 \text{ mAdc}$)		—	855	
($I_F = 60 \text{ mAdc}$)		—	1000	
($I_F = 150 \text{ mAdc}$)		—	1250	
Reverse Recovery Time ($I_F = I_R = 10 \text{ mAdc}, R_L = 100 \Omega, I_{R(REC)} = 1.0 \text{ mAdc}$) (Figure 1)	t_{rr}	—	6.0	ns

1. FR-5 = $1.0 \times 0.75 \times 0.062 \text{ in.}$

2. Alumina = $0.4 \times 0.3 \times 0.024 \text{ in.}$ 99.5% alumina.

BAW56WT1



- Notes: 1. A 2.0 kΩ variable resistor adjusted for a Forward Current (I_F) of 10mA.
 2. Input pulse is adjusted so $I_{R(peak)}$ is equal to 10mA.
 3. $t_p \gg t_{rr}$

Figure 1. Recovery Time Equivalent Test Circuit

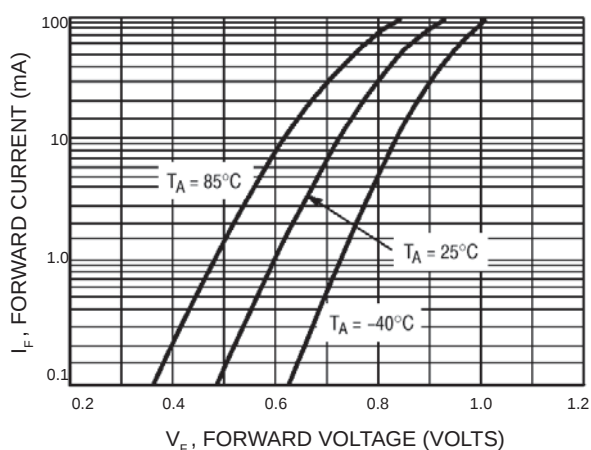


Figure 2. Forward Voltage

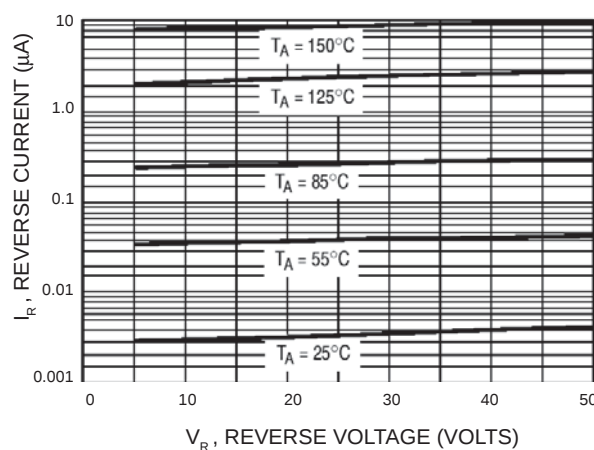


Figure 3. Leakage Current

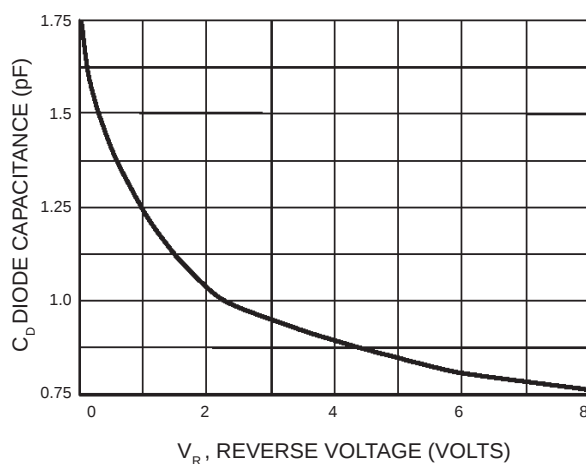


Figure 4. Capacitance