

SANYO

No.4137

2SC4890

NPN Triple Diffused Planar Silicon Transistor
 Very High-Definition CRT Display
 Horizontal Deflection Output Applications

Features

- High Speed ($t_f = 100\text{ns}$ typ).
- High reliability (Adoption of HVP process).
- High breakdown voltage ($V_{CBO} = 1500\text{V}$).
- Adoption of MBIT process.

Absolute Maximum Ratings at $T_a = 25^\circ\text{C}$

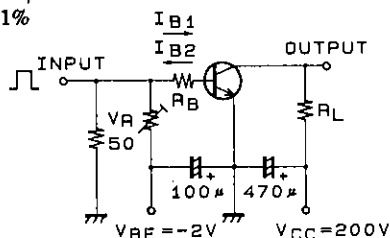
Collector-to-Base Voltage	V_{CBO}	1500	V
Collector-to-Emitter Voltage	V_{CEO}	800	V
Emitter-to-Base Voltage	V_{EBO}	6	V
Collector Current	I_C	12	A
Peak Collector Current	i_{cp}	30	A
Collector Dissipation	P_C	3.0	W
$T_c = 25^\circ\text{C}$			
Junction Temperature	T_j	75	W
Storage Temperature	T_{stg}	150	$^\circ\text{C}$
		-55 to +150	$^\circ\text{C}$

Electrical Characteristics at $T_a = 25^\circ\text{C}$

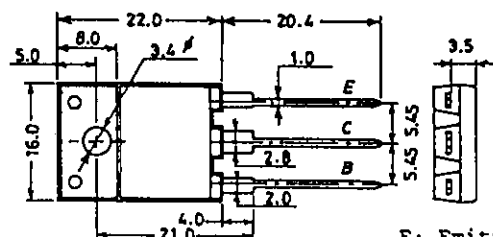
		min	typ	max	unit
Collector Cutoff Current	I_{CBO}			10	μA
Collector Cutoff Current	I_{CES}			1.0	mA
Collector Sustain Voltage	$V_{CEO(sus)}$	800			V
Emitter Cutoff Current	I_{EBO}			1.0	mA
C-E Saturation Voltage	$V_{CE(sat)}$			5	V
B-E Saturation Voltage	$V_{BE(sat)}$			1.5	V
DC Current Gain	$h_{FE(1)}$	8		30	
	$h_{FE(2)}$	4		8	
Storage Time	t_{stg}			3.0	μs
Fall Time	t_f			0.2	μs

Switching Time Test Circuit

PW = 20 μs
 DC $\leq 1\%$



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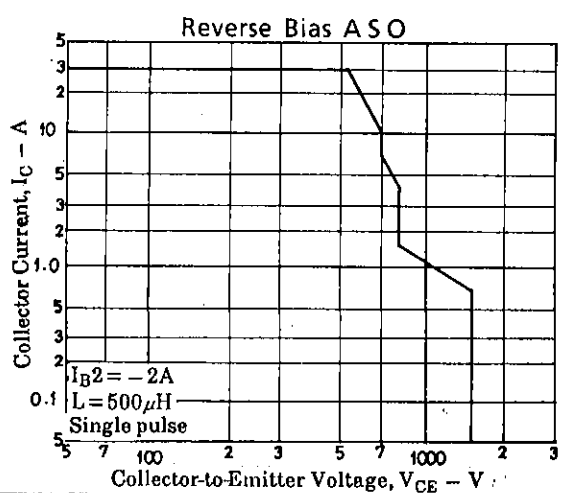
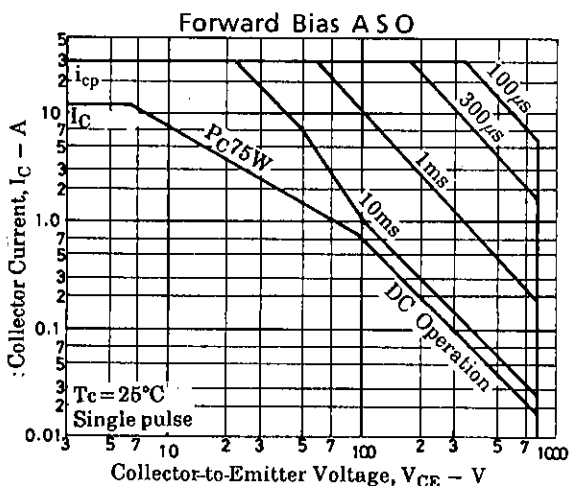
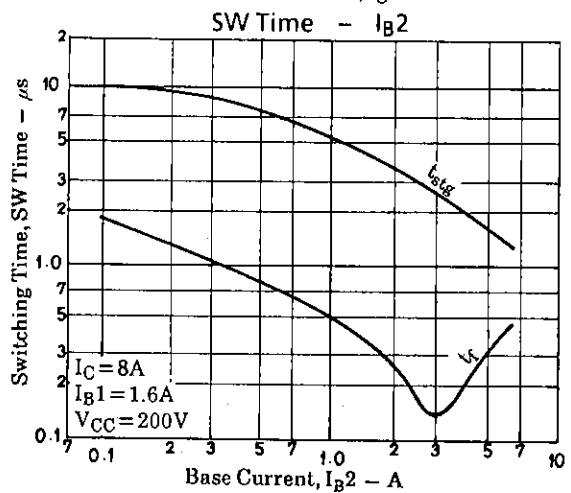
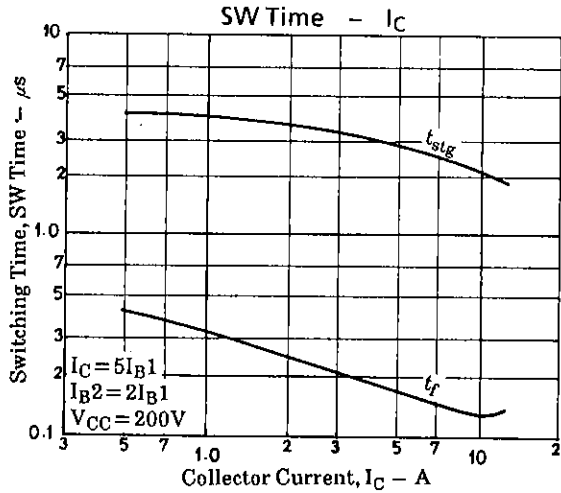
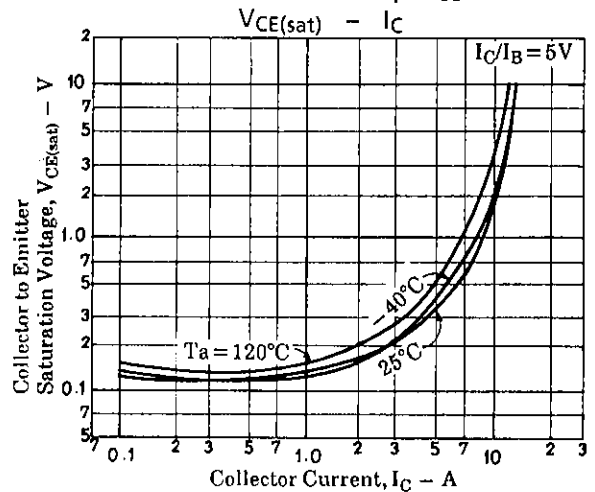
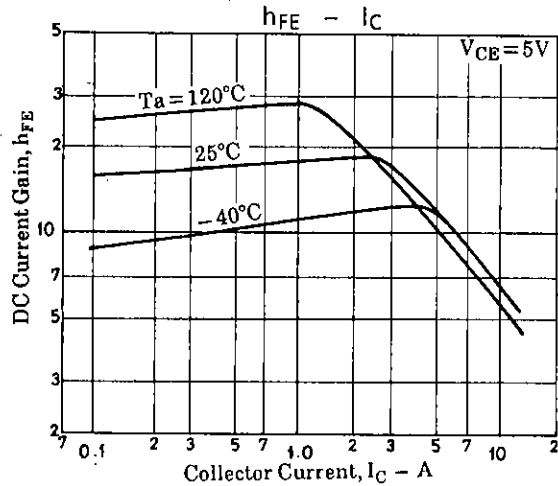
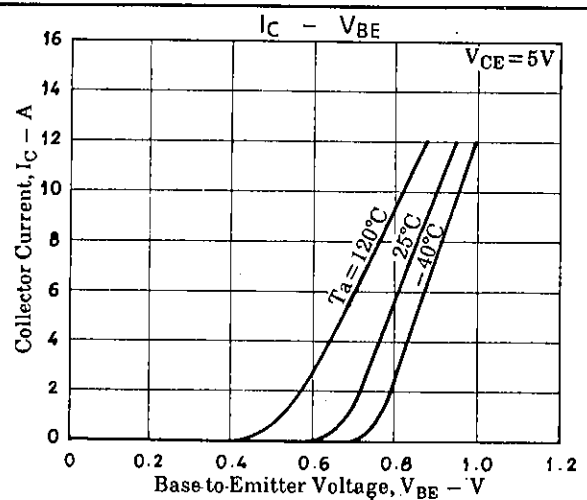
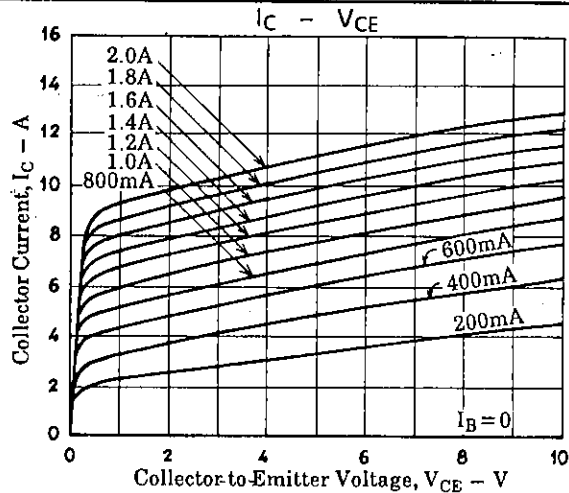
Unit (resistance: Ω , capacitance: F)**Package Dimensions 2039A**
(unit: mm)

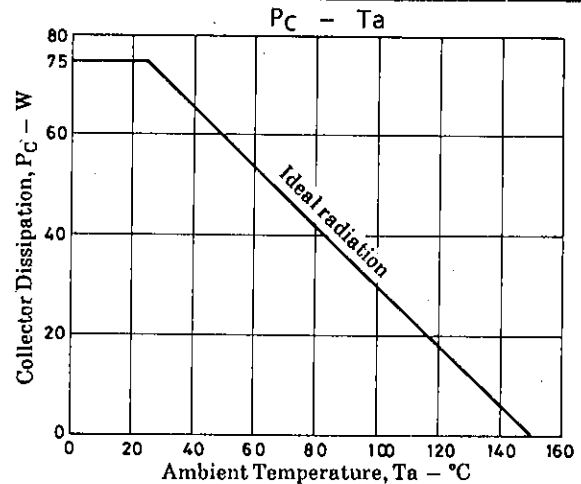
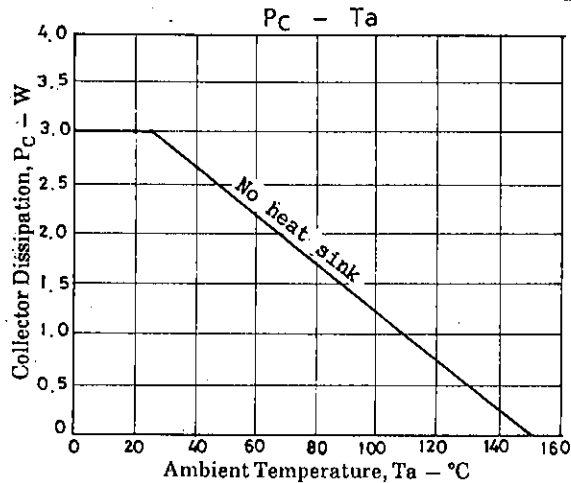
E: Emitter
 C: Collector
 B: Base

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