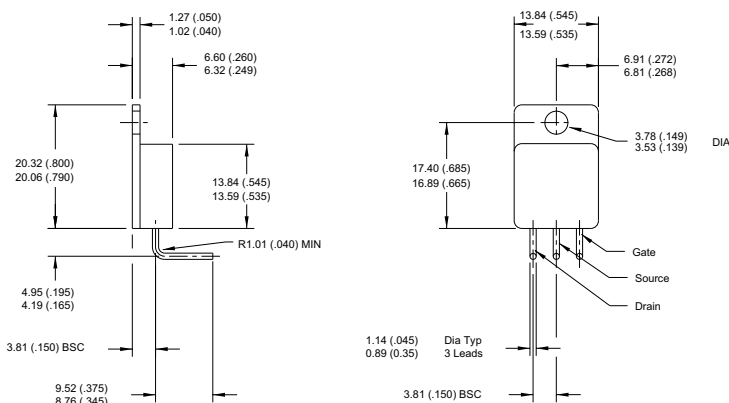


MECHANICAL DATA

Dimensions in mm (inches)



N-CHANNEL POWER MOSFET

V_{DSS} 200V

$I_{D(cont)}$ 27.4A

$R_{DS(on)}$ 0.100 Ω

FEATURES

- N-CHANNEL MOSFET
- HIGH VOLTAGE
- INTEGRAL PROTECTION DIODE
- HERMETIC ISOLATED TO-254 PAC
- CERAMIC SURFACE MOUNT PACK OPTION

TO-254 Metal Package

Pin 1 – Drain Pin 2 – Source Pin 3 – Gate

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise stated)

V_{GS}	Gate – Source Voltage		$\pm 20\text{V}$
I_D	Continuous Drain Current	@ $V_{GS} = 10\text{V}$, $T_C = 25^\circ\text{C}$	27.4A
		@ $V_{GS} = 10\text{V}$, $T_C = 100^\circ\text{C}$	17A
I_{DM}	Pulsed Drain Current		110A
P_D	Max. Power Dissipation	@ $T_C = 25^\circ\text{C}$	150W
	Linear Derating Factor		1.2W / $^\circ\text{C}$
I_L	Avalanche Current , Clamped ¹		27.4A
dv / dt	Peak Diode Recovery ²		5.5V / ns
$R_{\theta JC}$	Thermal Resistance Junction – Case		0.83 $^\circ\text{C}$ / W
$R_{\theta JA}$	Thermal Resistance Junction – Ambient		48 $^\circ\text{C}$ / W
$R_{\theta CS}$	Thermal Resistance Case – Sink		0.21 $^\circ\text{C}$ / W typ.
T_J , T_{STG}	Operating Junction and Storage Temperature Range		-55 to 150 $^\circ\text{C}$
T_L	Lead Temperature (1.6mm from case for 10s)		300 $^\circ\text{C}$

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise stated)

Parameter		Test Conditions		Min.	Typ.	Max
STATIC ELECTRICAL RATINGS						
BV _{DSS}	Drain – Source Breakdown Voltage	V _{GS} = 0	I _D = 1mA	200		
ΔBV _{DSS}	Temperature Coefficient of Breakdown Voltage	Reference to 25°C I _D = 1mA			0.28	
R _{DS(on)}	Static Drain – Source On–State Resistance ²	V _{GS} = 10V	I _D = 17A			0.100
		V _{GS} = 10V	I _D = 27.4A			0.105
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS}	I _D = 250μA	2		4
g _{fs}	Forward Transconductance ²	V _{DS} ≥ 15V	I _{DS} = 27.4A	9		
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} = 0	V _{DS} = 0.8BV _{DSS}			25
			T _J = 125°C			250
I _{GSS}	Forward Gate – Source Leakage	V _{GS} = 20V				100
I _{GSS}	Reverse Gate – Source Leakage	V _{GS} = –20V				–100
DYNAMIC CHARACTERISTICS						
C _{iss}	Input Capacitance	V _{GS} = 0 V _{DS} = 25V f = 1MHz			3500	
C _{oss}	Output Capacitance				700	
C _{rss}	Reverse Transfer Capacitance				110	
C _{DC}	Drain – Case Capacitance				12	
Q _g	Total Gate Charge	V _{GS} = 10V		55		115
Q _{gs}	Gate – Source Charge	I _D = 27.4A		8		22
Q _{gd}	Gate – Drain (“Miller”) Charge	V _{DS} = 0.5BV _{DSS}		30		60
t _{d(on)}	Turn– On Delay Time	V _{DD} = 100V I _D = 27.4A R _G = 2.35Ω				35
t _r	Rise Time					190
t _{d(off)}	Turn–Off Delay Time					170
t _f	Fall Time					130
SOURCE – DRAIN DIODE CHARACTERISTICS						
I _S	Continuous Source Current					27.4
I _{SM}	Pulse Source Current ¹					110
V _{SD}	Diode Forward Voltage ²	I _S = 27.4A	T _J = 25°C V _{GS} = 0			1.9
t _{rr}	Reverse Recovery Time ²	I _F = 27.4A	T _J = 25°C			950
Q _{rr}	Reverse Recovery Charge ²	d _i / d _t ≤ 100A/μs V _{DD} ≤ 50V				9.0
t _{on}	Forward Turn–On Time			Negligible		
PACKAGE CHARACTERISTICS						
L _D	Internal Drain Inductance	Measured from 6mm down drain lead to centre of die			8.7	
L _S	Internal Source Inductance	Measured from 6mm down source lead to source bond pad			8.7	