

Quality System for producing discrete semiconductor devices and integrated circuits conforms to the requirements of ISO 9002-96

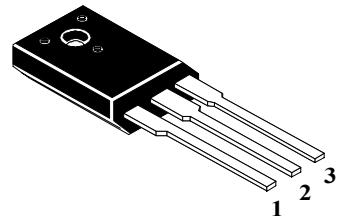
78F12C

**THREE-Terminal Positive
VOLTAGE REGULATOR IC**

FEATURES:

- OUTPUT CURRENT IN EXCESS OF 1A;
- NO EXTERNAL COMPONENTS REQUIRED;
- INTERNAL SHORT CIRCUIT CURRENT LIMITING;
- INTERNAL THERMAL OVERLOAD PROTECTION;
- OUTPUT TRANSISTOR SAFE-AREA COMPENSATION;
- OUTPUT VOLTAGE OFFERED IN 4% TOLERANCE

TO-126



Pin #	Symbol	Function
1	IN	Input
2	GND	Ground
3	OUT	Output

ABSOLUTE MAXIMUM RATINGS (Ta= 25°C)

Characteristic	Symbol	Unit	Value
Input Voltage	Vin	V	35
Maximum Dissipated Power (with heat sink)	Ptot(max)	W	12
Maximum Dissipated Power (without heat sink)	Ptot(max)	W	1.25
Thermal Resistance Junction to Case	θjC	°C/W	7.5
Thermal Resistance, Junction to Air	θjA	°C/W	100
Operating Junction Temperature Range	T _j	°C	0 to 150
Operating Ambient Temperature Range	T _a	°C	-10 to 70

ELECTRICAL CHARACTERISTICS

(Vin =19V, Io=0.5A, Ci =0.33μF, Co = 0.1μF, T_j =0 to + 125°C, unless otherwise noted.)

Characteristic	Symbol	Norm			Unit
		Min	Typ	Max	
Output Voltage (T _j =25°C)	V _o	11.5		12.5	V
Output Voltage (5.0mA ≤ I _o ≤ 1.0A, P _o ≤12W) 14.5V ≤ V _{in} ≤ 27V	V _o	11.4		12.6	V
Line Regulation (T _j =+25°C) 14.5V ≤ V _{in} ≤ 30 V 16.0V ≤ V _{in} ≤ 22 V	ΔV _v			240 120	mV
Load Regulation (T _j =+25°C) 5.0mA ≤ I _o ≤ 1.0A 0.25A ≤ I _o ≤ 0.75A	ΔV _i			240 120	mV
Quiescent Current (T _j =+25°C)	I _b			6.0	mA
Quiescent Current Change 14,5 V ≤ V _{in} ≤ 30 V 5 mA ≤ I _o ≤ 1.0 A	ΔI _b			1.0 0.5	mA
Dropout Voltage (T _j =+25°C)	V _{i-Vo}		2.0		V
Short Circuit Current Limit (T _a =+25°C), V _{in} =35V	I _{sc}		0.3		A
Peak Output Current (T _j =+25°C)	I _{max}		1.5		A
Average Temperature Coefficient of Output Voltage	TCV _o		0.7		mV/°C